

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Changming Jin et al.

Docket Number: TI-26111

Serial No.: 09/178,249

Examiner: G. Lee

Filed: 10/23/1998

Art Unit: 2825

11/GAU-875/\$  
#5  
Scripps  
7/20/00

Integrated Circuit Dielectric and Method

**NOTICE OF APPEAL**



Assistant Commissioner  
for Patents  
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.	
<u>Cecelia Grant</u>	<u>7-13-00</u>
	Date

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner dated **04/13/2000** all rejected claims.

The item(s) checked below are appropriate:

1. \_\_\_\_\_ An extension of time to respond to the final rejection  
\_\_\_\_\_ was granted on  
\_\_\_\_\_ is requested for month(s).
2. \_\_\_\_\_ A timely response to the final rejection has been filed.
3.   X   Fee \$300.00:  
\_\_\_\_\_ Not required (Fee paid in prior appeal).

**RECEIVED**  
JUL 19 2000  
TECHNOLOGY CENTER 2800

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  X   The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to the Deposit Account of Texas Instruments Incorporated, Account No. 20-0668. An original and one copy of this sheet are enclosed.

Signature [Rule 191(b)]

Carlton H. Hoel  
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(to which correspondence is to be sent)